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Cray's Expanding Presence in the EMEA Region HPC User Forum Munich, 15/16 October 2015

10/23/2015

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Cray Global success

- Revenue: in 2014 Global revenue \$561.6M
 - EMEA accounts for 34%
- Staff: from 2005 to 2015 increased by 62%
 - Cray EMEA staff has increased by 111%
- Dominance in very large systems: top500
 - EMEA: CSCS, KAUST, Met Office ...
- New customer wins in Enterprise/Commercial
 - Energy, manufacturing, financial services

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TOP500

- Worldwide Cray is a clear leader in performance, claiming a 24% share of installed total performance
- In EMEA: Cray delivered 27% of the total EMEA performance in the Top500
- Cray is the most focused solution provider in EMEA for the combination of big data and supercomputing
- Cray arguably the leader in high performance computing and storage solutions in EMEA



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Cray Systems and Storage in EMEA

- PGS, Largest Commercial Supercomputer
- "Shaheen II"- KAUST Saudi Arabia
- Multiphase system UK Met Office
- "Hornet" HLRS Germany
- "Beskow" KTH/PDC Sweden
- "Sisu" CSC Finland
- "Piz Daint" CSCS Switzerland
- "ARCHER" EPSRC UK
- "Gottfried" RRZN/LUIS Germany
- "Konrad" ZIB- Germany
- DWD
- "Ventus" & "Anemos" ECMWF

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Top100 – Number of Systems by Geography



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Map of Cray Systems and Offices in EMEA



Cray EMEA has offices in Bristol: EMEA Headquarters (Sales, Service, R&D, G&A) Basel: Field Office (Sales, Service) Dubai: Field Office (Sales, Service)





Recent History Cray in EMEA

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Cray Builds Computational Tools that Help our Customers Solve the World's Most Challenging Problems



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http://www.epigram-project.eu

- EU FP7 Network: Exascale ProGRAmming Models
- Consortium has
 - Leading European HPC centers
 - PDC/KTH, EPCC, CSC-Fi
 - Hardware partner
 - Cray
 - Communication model specialists
 - Fraunhofer, TU-Wien
- Continues co-design approach
 - Focussing on MPI and GASPI
 - 2 apps: Nek5000, iPIC3D
- EPiGRAM builds on the CRESTA work
 - Three year project, finishing in Sep. 2016



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Cray OpenACC training at CSCS

Human Brain Project: Co-design for a new application class

- Develop a blueprint and prototype of a custom-designed system for neuroscientific grand-challenge simulations
- Different memory/compute ratio than standard HPC
- Support super-massively parallel computation
- System designed for interactive simulation and visualisation
- Workload manager for complex scientific workflows

Visualisation of 1000 cells in a cortical circuit

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QPACE4 – Co-design for ARM technology exploration

- Cray announced at SC'14 that we will "[...] evaluate alternative processor design points, including the potential use of 64-bit ARM[®]"
- QPACE is a series of co-design projects of the Universities of Regensburg and Wupppertal for QCD codes
- The Universities and Cray plan to submit a proposal to the German Research Foundation evaluating 64-bit ARM with a prototype Cray SW environment
- CERL will temporarily host students of the Universities for the purpose of the project





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Eiger : A new approach for complex memory hierarchies

• Minimize bytes of data moved in memory hierarchy

- What new SW tools, approaches and maths are required?
- Collaboration between Cray, ETH Zürich, CSCS.

• SW Tools : Compiler alone can't solve the data problem

• Need new SW intelligence that adapts your code to the system architecture

• Approaches : "Model-Based optimization"

- Auto-tuning is effective but not useful
- We believe "Memory hierarchy can be modeled"
- Maths : Use Mathematical Optimization
 - No longer rely solely on expert hand-tuning

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Our Vision...

Build a world-class integrated supercomputing environment that enables transformational computing across a broad set of science, engineering and advanced analytics (big data) applications



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